

Applicant:

Yoshihisa Nagano et al.

: Art Unit: 2815

Serial No.:

09/103,873

: Examiner: G. Eckert II

Filed:

June 24, 1998

: Attn: Box Non-Fee

FOR: SEMICONDUCTOR DEVICE AND

Amendment

METHOD FOR FABRICATING THE SAME

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents and Trademarks Washington, DC 20231

SIR:

This is in response to the Restriction Requirement stated in the Office Letter dated September 9, 1998.

The Examiner requires that the claims of either Group I or Group II be elected for prosecution. Applicant elects to prosecute Group I (claims 1-10, drawn to a semiconductor device). This election of claims is made without traverse. CROLLB STATE

Respectfully submitted,

Andrew L Ney, Reg. No. 20

Attorney for Applicants

ALN:aw

Dated: September 25, 1998

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The Assistant Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. 18-0350 of any fees associated with this communication.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail. with sufficient postage, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C.

20231 on: